



HFZT

2315

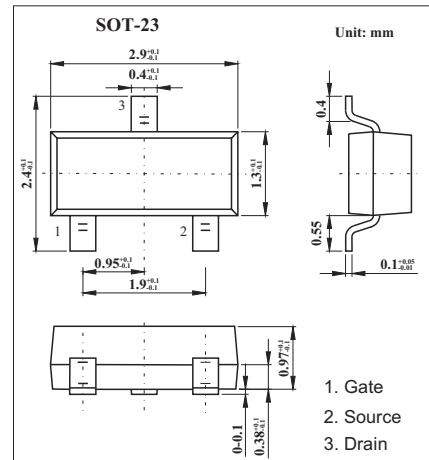
SOT-23 Plastic-Encapsulate MOSFETS

Features

- P-Channel 1.8-V (G-S) MOSFET

MECHANICAL DATA

- Case style:SOT-23molded plastic
- Mounting position:any



MAXIMUM RATINGS AND CHARACTERISTICS

@ 25°C Ambient Temperature (unless otherwise noted)

Parameter	Symbol	5 sec	Steady State	Unit
Drain-Source Voltage	V _{DS}	-	-12	V
Gate-Source Voltage	V _{Gs}	-	±8	V
Continuous Drain Current (TJ=150 °C) TA=25°C TA=70°C	I _D	-3.85 -3.0	-3.0 -2.45	A
Pulsed Drain Current	I _{DM}	-	-12	A
Continuous Source Current (diode conduction) *2	I _S	-1.0	-0.62	A
Power Dissipation TA=25°C TA=70°C	P _D	1.19 0.76	0.75 0.48	W
Junction Temperature	T _j	-	150	°C
Storage Temperature	T _{stg}	-	-55 to +150	°C

* Surface Mounted on FR4 Board.

Thermal Resistance Ratings Ta = 25°C

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient *1	R _{thJA}	85	105	C/W
Maximum Junction-to-Ambient *2 Steady State		130	166	
Maximum Junction-to-Foot (Drain) Steady State	R _{thJF}	60	75	

* 1. Surface Mounted on FR4 Board, t ≤ 5 sec.

* 2. Surface Mounted on FR4 Board.

MOSFET ELECTRICAL CHARACTERISTICS Ta=25 °C unless otherwise specified

Parameter	Symbol	Testconditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{Gs} = 0 V, I _D = -10 μA	-12			V
Gate Threshold Voltage	V _{Gs(th)}	V _{DS} = V _{Gs} , I _D = -250 μA	-0.45		-0.9	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{Gs} = ±8 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -12 V, V _{Gs} = 0 V			-1	
		V _{DS} = -12 V, V _{Gs} = 0 V, T _j = 55 °C			-10	μA
On-State Drain Current	I _{D(on)}	V _{DS} ≤ -5 V, V _{Gs} = -4.5 V	-6			A
		V _{DS} ≤ -5 V, V _{Gs} = -2.5 V	-3			
Drain-Source On-State Resistance	R _{DSS(on)}	V _{Gs} = -4.5 V, I _D = -3.85 A	0.040	0.050		Ω
		V _{Gs} = -2.5 V, I _D = -3.4 A	0.05	0.065		
		V _{Gs} = -1.8 V, I _D = -2.7 A	0.071	0.100		
Forward Transconductance	g _f s	V _{DS} = -5 V, I _D = -3.85 A		7		S
Diode Forward Voltage	V _{SD}	I _S = -1.6 A, V _{Gs} = 0 V			-1.2	V
Total Gate Charge	Q _g		8	15		nC
Gate-Source Charge	Q _{gs}	V _{DS} = -6V ,V _{Gs} = -4.5 V , I _D = -3.85 A	1.1			
Gate-Drain Charge	Q _{gd}		2.3			
Input Capacitance	C _{iss}		715			pF
Output Capacitance	C _{oss}	V _{DS} = -6V ,V _{Gs} = 0 , f = 1 MHz	275			
Reverse Transfer Capacitance	C _{rss}		200			
Turn-On Time	t _{d(on)}		15	20		ns
	t _r		35	50		
Turn-Off Time	t _{d(off)}	V _{DD} = -6V , R _L = 6Ω , I _D = -1A , V _{GEN} = -4.5V , R _G = 6Ω	50	70		
	t _f		50	75		

* Pulse test: PW ≤ 300 μs duty cycle ≤ 2%.

Marking	M5
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